

### Bare Silicon

### Indexed Silicon Chips

## Sindex™ Chips

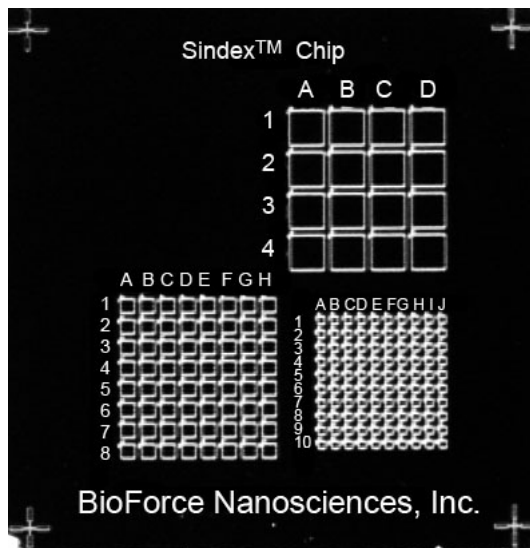
The Sindex™ chip is a 4x4 mm silicon substrate containing topographically defined pads that are arrayed within an alphanumeric indexing system. The pads are flat and smooth, making them ideal for fluorescence microscopy and atomic force microscopy. The indexing system allows precise relocation of specific positions on the chip.

There are three patterned regions on each chip: 50 μm, 100 μm, and 200 μm. There are two base surface functionality chips Sindex-Si (silicon coated only) and Sindex SiO (silicon w/ 2 μm Oxide layer). On the two base Sindex chips, there are several added functionality options in two categories: metal coated or chemically functionalized.

Sindex™ chips are offered in two grades:

**AFM grade**—one out of every 10 chips will be imaged by AFM to ensure that they meet the roughness specification and no particles are larger than 20 nm.

**Optical grade**—one out of every 10 chips inspected by optics and no particles present are larger than 1 μm.



#### Specifications:

Surface Functionality	Silicon (Si)
Grid Type	50 μm, 100 μm, and 200 μm square
Array No.	100 (10x10), 64 (8 x8), and 16 (4x4)
Chip size	4x4 mm 480 μm thick
Spacing	20 μm
Etch Depth	1 μm
Coating	None
Chemistry	None
RMS Roughness	0.2 nm

Silicon wafers are P type boron doped wafers with a resistivity of 10 to 20 Ohm/cm<sup>2</sup>.

### Price List:

Set of 10 Sindex™ chips - \$100 + shipping and handling  
AFM Certification - \$50.00